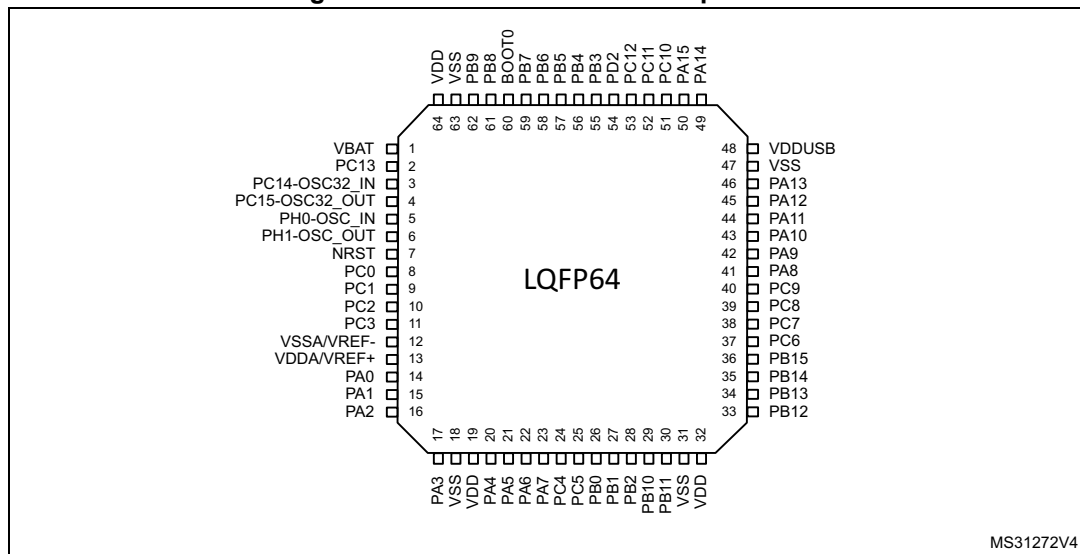


Figure 9. STM32L476Rx LQFP64 pinout⁽¹⁾

MS31272V4

1. The above figure shows the package top view.

Table 14. Legend/abbreviations used in the pinout table

Name	Abbreviation	Definition
Pin name	Unless otherwise specified in brackets below the pin name, the pin function during and after reset is the same as the actual pin name	
Pin type	S	Supply pin
	I	Input only pin
	I/O	Input / output pin
I/O structure	FT	5 V tolerant I/O
	TT	3.6 V tolerant I/O
	B	Dedicated BOOT0 pin
	RST	Bidirectional reset pin with embedded weak pull-up resistor
	Option for TT or FT I/Os	
	_f ⁽¹⁾	I/O, Fm+ capable
	_l ⁽²⁾	I/O, with LCD function supplied by V _{LCD}
	_u ⁽³⁾	I/O, with USB function supplied by V _{DDUSB}
	_a ⁽⁴⁾	I/O, with Analog switch function supplied by V _{DDA}
	_s ⁽⁵⁾	I/O supplied only by V _{DDIO2}
Notes	Unless otherwise specified by a note, all I/Os are set as analog inputs during and after reset.	
Pin functions	Alternate functions	Functions selected through GPIOx_AFR registers
	Additional functions	Functions directly selected/enabled through peripheral registers

1. The related I/O structures in [Table 15](#) are: FT_f, FT_fa, FT_fl, FT fla.