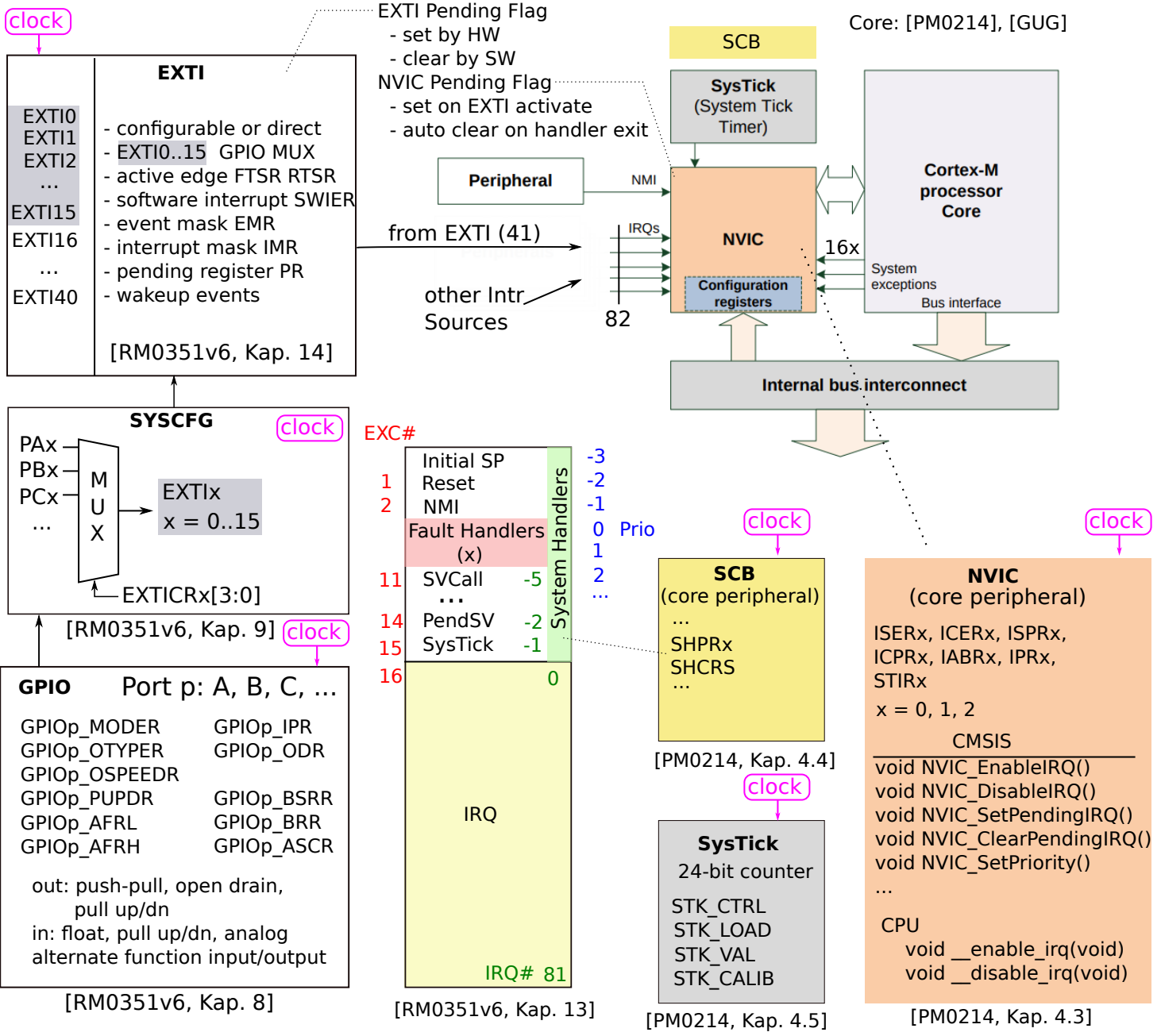
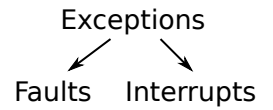


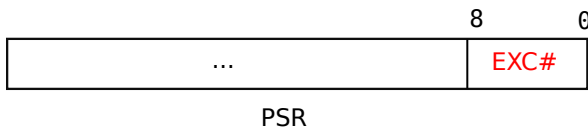
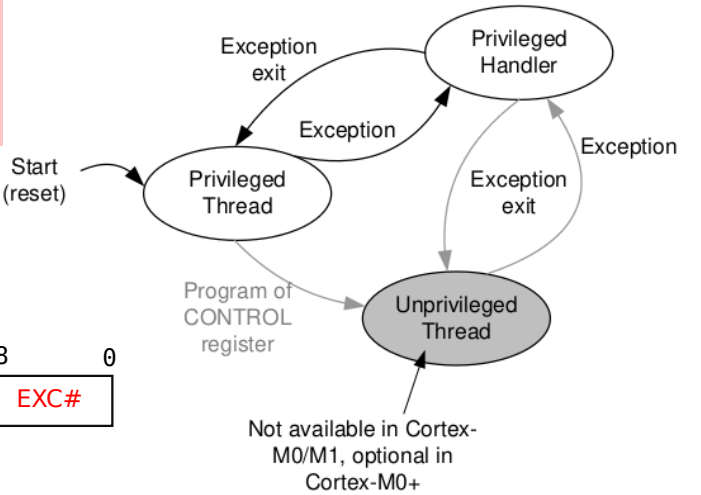
STM32 Exception Processing (STM32L476)



RCC Reset and Clock Control

RCC_CR, RCC_CFGR, RCC_CIER, RCC_PLLCFGR, RCC_AHB2ENR, RCC_APB1ENR1, RCC_CSR, RCC_BDCR, viele weitere ...
[RM0351v6, Kap. 6]

(x) Fault Handlers
- HardFault
- MemManageFault
- BusFault
- UsageFault



Lit.: <http://hhoegl.informatik.hs-augsburg.de/es2/skript>
<http://hhoegl.informatik.hs-augsburg.de/hhwiki/EmbeddedSystemsDocs>